

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("6587415" "5982718").pn.	USPAT	OR	OFF	2005/03/23 10:56
L2	15	("5050016" "5170299" "5255136" "5384671" "5420730" "5477103" "5544135" "5796535" "5903410" "5963387" "5966258" "6021012" "6111710" "6155199" "6525615").pn.	USPAT	OR	OFF	2005/03/23 13:06
L3	14	("6483789" "6108153" "5477103" "6754019" "6487032" "6021012" "6594103" "6545836" "6115199" "6760173" "4271519" "6392831" "6680807" "6122131").pn.	USPAT	OR	OFF	2005/03/23 13:06
L4	2	"20030026017" "20030026363"	US-PGPUB	OR	OFF	2005/03/23 13:06
L5	1	(VGA (variable adj2 gain adj2 amplifier)) same (read adj5 channel) same (AGC (automatic adj2 gain adj2 control)) same (PLL (phase adj 2 lock\$3 adj2 loop)) same (servo)	USPAT	OR	ON	2005/03/23 12:55
L6	13	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) same (servo)	USPAT	OR	ON	2005/03/23 12:58
L7	2	(servo) same (integral near5 windup)	USPAT	OR	ON	2005/03/23 13:00
L8	92	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo)	USPAT	OR	ON	2005/03/23 12:58
L9	92	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	USPAT	OR	ON	2005/03/23 13:16
L10	78	("360"/\$.ccls.) and 9	USPAT	OR	ON	2005/03/23 13:04
L11	95	(limit\$3 filter\$4) and (integral near5 windup)	USPAT	OR	ON	2005/03/23 13:00
L12	0	10 and 11	USPAT	OR	ON	2005/03/23 13:00
L13	33	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	US-PGPUB	OR	ON	2005/03/23 13:00

L14	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	EPO	OR	ON	2005/03/23 13:00
L15	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	JPO	OR	ON	2005/03/23 13:00
L16	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	DERWENT	OR	ON	2005/03/23 13:01
L17	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4)	IBM_TDB	OR	ON	2005/03/23 13:01
L18	81	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	USPAT	OR	ON	2005/03/23 13:03
L19	31	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	US-PGPUB	OR	ON	2005/03/23 13:03
L20	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	EPO	OR	ON	2005/03/23 13:03
L21	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	JPO	OR	ON	2005/03/23 13:03

L22	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	DERWENT	OR	ON	2005/03/23 13:04
L23	0	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4) and (register or buffer)	IBM_TDB	OR	ON	2005/03/23 13:04
L24	69	("360"/\$.ccls.) and 18	USPAT	OR	ON	2005/03/23 13:16
L25	92	((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))) and (servo) and (limit\$3 or filter\$4 or equaliz\$4)	USPAT	OR	ON	2005/03/23 13:16
L26	78	("360"/\$.ccls.) and 25	USPAT	OR	ON	2005/03/23 13:22
L27	648	(servo near10 demodulat\$4) and (robustness or enhancement or better or best)	USPAT	OR	ON	2005/03/23 13:20
L28	34	((servo near10 demodulat\$4) and (robustness enhancement better or best) and (filter\$4 equaliz\$4 limit\$4)) and (((VGA (variable adj2 gain adj2 amplifier)) and (read adj5 channel) and (AGC (automatic adj2 gain adj2 control)) and (PLL (phase adj 2 lock\$3 adj2 loop))))	USPAT	OR	ON	2005/03/23 13:22
L29	31	("360"/\$.ccls.) and 28	USPAT	OR	ON	2005/03/23 13:22

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Ehrlich M.	Ehrlich M. R.	Ehrlich N.	Ehrlich R.	Ehrlich R. D.
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- 3) Limit the results by selecting Search Options.
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servo<and>demodulation<and>read
<and>channel

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Index Indication

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Number Search

Applicant, Title of invention, Abstract — e.g. computer semiconductor

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servo

AND

AND

demodulation

AND

AND

read channel

AND

AND

Date of publication of application — e.g. 19980401 - 19980405

AND

IPC — e.g. D01B7/04 A01C11/02

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